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THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Joseph A. Iadanza

Group Art Unit: 2815

Filed: 02/26/2002

Examiner: Ortiz, Edgardo

Serial No.: 09/683,872

Docket No.: BUR920010100US1

Title: METHOD OF CONNECTING CORE I/O PINS TO BACKSIDE CHIP I/O PADS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Request for Reconsideration

Sir:

This Request for reconsideration is in response to the Final Office Action mailed

March 28, 2005.

09/683,872

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